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DMTF Master MIF

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11   Copyright Notice

12   Copyright © 1996-2020 DMTF. All rights reserved.

34   Foreword

35   The *DMTF Master* MIF (DSP8014) was prepared by the Technical Committee.

36   DMTF is a not-for-profit association of industry members dedicated to promoting enterprise and systems
37   management and interoperability. For information about the DMTF, see http://www.dmtf.org.

38   **ANNEX A** (**informative**)

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40   **Change log**

41   The change log entries from 1/1/1996 to 7/7/2004 are contained within the master.mif file, itself. See the
42   section delimited by the text "Change History".

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Version	Date	Description
	12/5/2002	Created DMTF|Processor|017 • Added new enums to Processor Family: • Enum: 184 = "Intel(R) Itanium(R) 2" • Enum: 131 = "AMD Athlon(TM) 64 Processor Family" • Enum: 132 = "AMD Opteron(TM) Processor Family" • Changed wording in descriptive text (Processor Family): • Enum: 182 = " AMD Athlon(TM) XP Processor Family" • Enum: 183 = " AMD Athlon(TM) MP Processor Family • Added new enums to Processor Upgrade • Enum: 16 = "Socket 754" • Enum: 17 = "Socket 940"
	6/21/2003	Changed copyright year to 2003 Created DMTF|Processor|018 • Added new enums to Processor Family: • Enum 185 = "Intel(R) Pentium(R) M processor"
	9/18/2003	Created DMTF|Processor|019 • Added new enums to Processor Family: • Enum 122 = "Efficeon(TM) TM8000 Family"
	4/19/2004	Created DMTF|Processor|020 • Added new enums to Processor Upgrade: • Enum: 18 = "Socket 939"
		Changes not documented
1.0.0	10/21/2019	Created DMTF|System Slot|006 • Added bit definition to "Slot Type" attribute for CXL Flexbus • Bit 20 = "CXL Flexbus 1.0 slot"

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