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5 **Management Component Transport Protocol**
6 **(MCTP) IDs and Codes**

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Foreword

63 The *Management Component Transport Protocol (MCTP) IDs and Codes* (DSP0239) was prepared by
64 the PMCI Working Group.

65 DMTF is a not-for-profit association of industry members dedicated to promoting enterprise and systems
66 management and interoperability.

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Introduction

68 This document presents a collection of IDs and codes that are used across the Management Component
69 Transport Protocol (MCTP) and transport binding specifications.

70 The MCTP defines a communication model intended to facilitate communication between:

- 71 • Management controllers and other management controllers
- 72 • Management controllers and management devices

73 The communication model includes a message format, transport description, message exchange
74 patterns, and configuration and initialization messages.

75 The *MCTP Base Protocol Specification* ([DSP0236](#)) describes the protocol and commands used for
76 communication within and initialization of an MCTP network. Associated with the *Base Protocol*
77 *Specification* are transport binding specifications that define how the MCTP base protocol and MCTP
78 control commands are implemented on a particular physical transport type and medium, such as
79 SMBus/I²C, PCI Express™ (PCIe) Vendor Defined Messaging (VDM), and so on.

80 Management Component Transport Protocol (MCTP) IDs and 81 Codes

82 1 Scope

83 The *Management Component Transport Protocol (MCTP) IDs and Codes* document provides a
84 consolidated list of major IDs and codes used across the MCTP protocol and transport binding
85 specifications. Only IDs and codes that are required by a particular specification should be included in
86 that specification. IDs and codes values for other specifications should not be repeated for reference.
87 Instead, a reference to this specification should be provided.

88 The following is an overview of the different sets of codes and identifiers (enumeration values) that are
89 specified in this document:

- 90 • **MCTP message type codes**
91 Collection of the message type codes used for MCTP messages
- 92 • **MCTP physical medium identifiers**
93 Collection of identifiers for the different types of physical media that have been defined
- 94 • **MCTP physical transport binding identifiers**
95 Collection of identifiers for the specifications that define the operation, formatting, addressing,
96 and encapsulation of MCTP packets over different physical media

97 2 Normative References

98 The following referenced documents are indispensable for the application of this document. For dated
99 references, only the edition cited applies. For undated references, the latest edition of the referenced
100 document (including any amendments) applies.

101 DMTF DSP0222, *Network Controller Sideband Interface (NC-SI) Specification 1.0*,
102 http://www.dmtf.org/standards/published_documents/DSP0222_1.0.pdf

103 DMTF DSP0236, *Management Component Transport Protocol (MCTP) Base Specification 1.0*, MCTP,
104 http://www.dmtf.org/standards/published_documents/DSP0236_1.0.pdf

105 DMTF DSP0237, *Management Component Transport Protocol (MCTP) SMBus²C Transporting Binding*
106 *Specification 1.0*, MCTP SMBus-I²C,
107 http://www.dmtf.org/standards/published_documents/DSP0237_1.0.pdf

108 DMTF DSP0238, *Management Component Transport Protocol (MCTP) PCIe VDM Transport Binding*
109 *Specification 1.0*, MCTP PCIe-V, http://www.dmtf.org/standards/published_documents/DSP0238_1.0.pdf

110 IPMI Consortium, *Intelligent Platform Management Interface Specification 1.5* Revision 1.1, February 20,
111 2002, http://download.intel.com/design/servers/ipmi/IPMIv1_5rev1_1.pdf

112 ISO/IEC Directives, Part 2, *Rules for the structure and drafting of International Standards*,
113 <http://isotc.iso.org/livelink/livelink?func=ll&objId=4230456&objAction=browse&sort=subtype>

114 PCI-SIG, *PCI Express Base Specification 1.1*, PCIeV1.1, March 28, 2005,
115 http://www.pcisig.com/members/downloads/specifications/pciexpress/PCI_Express_Base_11.pdf

- 116 PCI-SIG, *PCI Express Base Specification 2.0*, PCIeV2.0, December 20, 2006,
117 http://www.pcisig.com/members/downloads/specifications/pciexpress/PCI_Express_Base_2.pdf
- 118 Philips Semiconductors, *The I²C-Bus Specification v2.0*, I2C, December 1998
119 http://www.nxp.com/acrobat_download/literature/9398/39340011_20.pdf
- 120 RMII Consortium, *Reduced Media Independent Interface (RMII) Specification v1.2*, RMII, 1997,
121 http://www.national.com/appinfo/networks/files/rmii_1_2.pdf
- 122 SMBus, *System Management Bus (SMBus) Specification v2.0*, SMBus, 2000,
123 <http://www.smbus.org/specs/smbus20.pdf>

124 **3 Terms and Definitions**

125 Refer to [DSP0236](#) for terms and definitions that are used in the MCTP specifications.

126 **4 Symbols and Abbreviated Terms**

127 Refer to [DSP0236](#) for symbols and abbreviated terms that are used in the MCTP specifications.

128 **5 Conventions**

129 The conventions described in the following clauses apply to this specification.

130 **5.1 Reserved and Unassigned Values**

131 Unless otherwise specified, any reserved, unspecified, or unassigned values in enumerations or other
132 numeric ranges are reserved for future definition by the DMTF.

133 Unless otherwise specified, numeric or bit fields that are designated as reserved shall be written as 0
134 (zero) and ignored when read.

135 **5.2 Byte Ordering**

136 Unless otherwise specified, byte ordering of multi-byte numeric fields or bit fields is "Big Endian" (that is,
137 the lower byte offset holds the most significant byte, and higher offsets hold lesser significant bytes).

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139 **6 MCTP Message Type Codes**

140 Table 1 defines the values for the Message Type field for different message types transported through
 141 MCTP.

142 NOTE: A device that supports a given message type may not support that message type equally across all busses
 143 that connect to the device.

144 **Table 1 – MCTP Message Types**

Message Type	Message Type Code	Description
MCTP Control	0x00	Messages used to support initialization and configuration of MCTP communication within an MCTP network, as specified in DSP0236
Platform Level Data Model	0x01	Reserved for future Platform Level Data Model (PLDM) Message Type
NC-SI over MCTP	0x02	Reserved for NC-SI over MCTP Message Type
Vendor Defined – PCI	0x7E	Message type used to support VDMs where the vendor is identified using a PCI-based vendor ID. The specification of the initial Message Header bytes for this message type is provided within this specification. The specification of the format of this message is given in DSP0236 . Otherwise, the message body content is specified by the vendor, company, or organization identified by the given vendor ID.
Vendor Defined – IANA	0x7F	Message type used to support VDMs where the vendor is identified using an IANA-based vendor ID. This format uses an "Enterprise Number" that is assigned and maintained by the Internet Assigned Numbers Authority (IANA), www.iana.org , as the means of identifying a particular vendor, company, or organization. The specification of the format of this message is given in DSP0236 . Otherwise, the message body content is specified by the vendor, company, or organization identified by the given vendor ID.
Reserved	all other	Reserved

145 **7 MCTP Physical Medium Identifiers**

146 Table 2 defines a set of numbers that correspond to different media types that can be used with MCTP.
 147 The identifier is primarily used to identify which physical addressing format is used for MCTP packets on
 148 the bus.

149 **Table 2 – MCTP Physical Medium Identifiers**

Physical Media Identifier	Description
0x00	Unspecified
0x01	SMBus 2.0 100 kHz compatible
0x02	SMBus 2.0 + I ² C 100 kHz compatible
0x03	I ² C 100 kHz compatible
0x04	I ² C 400 kHz compatible
0x05 : 0x07	Reserved
0x08	PCIe 1.1 compatible
0x09	PCIe 2.0 compatible
0x0A	PCIe 2.1 compatible
0x0B	PCIe 3.0 compatible
0x0C : 0x0E	Reserved
0x0F	PCI compatible (PCI 1.0,2.0,2.1,2.2,2.3,3.0,PCI-X 1.0, PCI-X 2.0)
0x10	USB 1.1 compatible
0x11	USB 2.0 compatible
0x12	USB 3.0 compatible
0x13 : 0x17	Reserved
0x18	RMII / NC-SI (refer to DSP0222)
0x20	KCS ¹ / Legacy (Fixed Address Decoding)
0x21	KCS ¹ / PCI (Base Class 0xC0 Subclass 0x01)
0x22	Serial Host ² / Legacy (Fixed Address Decoding)
0x23	Serial Host ² / PCI (Base Class 0x07 Subclass 0x00)
0x24	Asynchronous Serial ³ (Between MCs and IMDs)
all other	Reserved
1. Keyboard Controller Style Interface – refer to DSP0236 . 2. Serial Host refers to a register based UART interface. 3. Asynchronous Serial refers to an 8-bit asynchronous bi-directional serial transmission media where characters are transmitted independently (i.e., each frame carries 8-bits of data).	

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151 8 MCTP Physical Transport Binding Identifiers

152 Table 3 defines as set of numbers that correspond to different media types that can be used with MCTP.
 153 The identifier indicates which physical addressing format is used for MCTP packets on the bus.

154 **Table 3 – MCTP Physical Transport Binding Identifiers**

MCTP Physical Transport Binding Identifier	Description
0x00	Reserved
0x01	MCTP over SMBus (DSP0237)
0x02	MCTP over PCIe VDM (DSP0238)
0x03	Reserved for MCTP over USB
0x04	MCTP over KCS
0x05	MCTP over Serial
0xFF	Vendor defined NOTE: A vendor-defined transport binding must meet the requirements in DSP0236 (in particular, when being bridged to or from standard MCTP transport binding and media combinations).
all other	Reserved

155 9 MCTP Host Interface Type Identifiers

156 Table 3 defines a set of numbers that correspond to different MCTP host interface types that can be used
 157 with MCTP. The identifier indicates which physical interface to transfer MCTP packets between the host
 158 and the management controller.

159 **Table 4 – MCTP Host Interface Type Identifiers**

MCTP Host Interface Type Identifier	Description
0x00	Reserved
0x01	Reserved
0x02	KCS: Keyboard Controller Style – refer to Intelligent Platform Management Interface Specification Section 9 Keyboard Controller Style (KCS) Interface
0x03	8250 UART Register Compatible
0x04	16450 UART Register Compatible
0x05	16550/16550A UART Register Compatible
0x06	16650/16650A UART Register Compatible
0x07	16750/16750A UART Register Compatible
0x08	16850/16850A UART Register Compatible
0xF0	OEM
all other	Reserved

160 10 Host Interface Protocol Identifiers

161 Table 3 defines a set of numbers that correspond to different protocols that can be used on a physical
 162 host interface. These protocol identifiers are used in SMBIOS Management Controller Host Interface
 163 Type 42 record as well as the ACPI MCHI description record.

164 **Table 5 – Host Interface Protocol Identifiers**

Protocol Identifier	Description
0x00	Reserved
0x01	Reserved
0x02	IPMI : Intelligent Platform Management Interface – refer to Intelligent Platform Management Interface Specification Appendix C1
0x03	MCTP : Management Component Transport Protocol – refer to DSP0236
0xF0	OEM
all other	Reserved

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Annex A (informative) Notation and Conventions

169 A.1 Notations

170 Examples of notations used in this document are as follows:

- 171 • 2:N In field descriptions, this will typically be used to represent a range of byte offsets
172 starting from byte two and continuing to and including byte N. The lowest offset is on
173 the left, the highest is on the right.
- 174 • (6) Parentheses around a single number can be used in message field descriptions to
175 indicate a byte field that may be present or absent.
- 176 • (3:6) Parentheses around a field consisting of a range of bytes indicates the entire range
177 may be present or absent. The lowest offset is on the left, the highest is on the right.
- 178 • PCIe Underlined, blue text is typically used to indicate a reference to a document or
179 specification called out in the "Normative References" section or to items hyperlinked
180 within the document.
- 181 • rsvd Abbreviation for "reserved." Case insensitive.
- 182 • [4] Square brackets around a number are typically used to indicate a bit offset. Bit offsets
183 are given as zero-based values (that is, the least significant bit [LSb] offset = 0).
- 184 • [7:5] A range of bit offsets. The most significant bit is on the left, the least significant bit is
185 on the right.
- 186 • 1b The lower case "b" following a number consisting of 0s and 1s is used to indicate the
187 number is being given in binary format.
- 188 • 0x12A A leading "0x" is used to indicate a number given in hexadecimal format.

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192**Annex B**
(informative)
Change Log

Version	Date	Description
1.0.0	07/28/2009	DMTF Standard Release
1.1.0	11/03/2009	Added Host Interface Type Identifiers Added Host Interface Protocol Identifiers Added reference to NC-SI and added clarification on physical medium identifiers

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